

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	290	inokuma-kazuyuki\$.in. fujii-toshiya\$.in. yamaguchi-takumi\$.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/01 07:42
S1	69	inokuma-kazuyuki\$.in. fujii-toshiya\$.in. yamaguchi-takumi\$.in.	US-PGPUB; USPAT	OR	OFF	2005/09/01 07:42
S2	13	imag\$4 near2 chip\$1 same (nmos or n adj mos)	USPAT	OR	OFF	2005/06/23 12:44
S3	8	imag\$4 near2 chip\$1 same (pmos or p adj mos)	USPAT	OR	OFF	2005/06/23 12:43
S4	1	S3 not S2	USPAT	OR	OFF	2005/06/23 12:43
S5	10	imag\$4 near2 chip\$1 and (nmos or n adj mos) with only	USPAT	OR	OFF	2005/06/23 12:55
S6	10	imag\$4 near2 chip\$1 and (pmos or p adj mos) with only	USPAT	OR	OFF	2005/06/23 12:48
S7	2035	250/208.1\$.ccls.	USPAT	OR	OFF	2005/06/23 12:48
S8	0	S7 and (pmos or p adj mos).ti.	USPAT	OR	OFF	2005/06/23 12:48
S9	0	S7 and (nmos or n adj mos).ti.	USPAT	OR	OFF	2005/06/23 12:48
S10	0	S7 and "same" with electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 12:49
S11	3	S7 and electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 12:50
S12	0	pmos same electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 12:50
S13	3	nmos same electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 13:05
S14	18	cmos same electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 12:51
S15	104	imag\$4 near2 chip\$1 same mos	USPAT	OR	OFF	2005/06/23 12:58
S16	25	imag\$4 near2 chip\$1 same mos with transistor\$1	USPAT	OR	OFF	2005/06/23 13:01
S17	0	(imag\$4 near2 chip\$1 with transistor\$1).ti.	USPAT	OR	OFF	2005/06/23 13:01
S18	65	(imag\$4 with transistor\$1).ti.	USPAT	OR	OFF	2005/06/23 13:01
S19	48	transistor\$1 with electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 13:29
S20	22	mos with electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 13:33
S21	14	S20 not S19	USPAT	OR	OFF	2005/06/23 13:29
S22	367	substrate with electric adj conductor\$1	USPAT	OR	OFF	2005/06/23 13:33
S23	358	S22 not S20	USPAT	OR	OFF	2005/06/23 13:36

S24	193	cmos with imag\$4 same (nmos or n adj mos or n adj channel)	USPAT	OR	OFF	2005/06/23 14:48
S25	25467	through adj current\$1	USPAT	OR	OFF	2005/06/23 14:43
S26	216	through adj current\$1 with eliminat\$4	USPAT	OR	OFF	2005/06/23 14:43
S27	1	cmos with imag\$4 and (nmos or n adj mos or n adj channel) near2 (exclusively)	USPAT	OR	OFF	2005/06/23 14:48
S28	0	cmos with imag\$4 and (nmos or n adj mos or n adj channel) near2 (all)	USPAT	OR	OFF	2005/06/23 14:49
S29	51	cmos with imag\$4 and (nmos or n adj mos or n adj channel) near2 (only)	USPAT	OR	OFF	2005/06/23 15:38
S30	1135	(257/686).CCLS.	USPAT	OR	OFF	2005/06/23 15:00
S31	2	("6521881" "6680525").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/23 15:01
S32	4	("4807021" "5952714" "5973337" "6049074").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/23 15:07
S33	1	("5952714").PN.	USPAT	OR	OFF	2005/06/23 15:15
S34	1	("6740870").PN.	USPAT	OR	OFF	2005/06/23 15:15
S35	8	("4329577" "4827118" "5122045" "5289002" "5591966" "5811797" "6091689" "6191359").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/23 15:18
S36	139	S30 and imag\$4	USPAT	OR	OFF	2005/06/23 15:18
S37	1	cmos with imag\$4 and (nmos or n adj mos or n adj channel) near2 (only)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:27
S38	14	imag\$4 and (nmos or n adj mos or n adj channel) near2 (only)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:28
S39	1	imag\$4 and (pmos or p adj mos or p adj channel) near2 (only)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/23 15:29
S40	17	cmos with imag\$4 and (pmos or p adj mos or p adj channel) near2 (only)	USPAT	OR	OFF	2005/06/23 15:34
S41	28	cmos with imag\$4 and (pmos or p adj mos or p adj channel) near2 ("same")	USPAT	OR	OFF	2005/06/23 15:34
S42	124	cmos with imag\$4 and (transistor\$1) near2 (only)	USPAT	OR	OFF	2005/06/24 06:10
S43	109	S42 not S29	USPAT	OR	OFF	2005/06/23 15:35

S44	294	imag\$4 with (nmos or n adj mos or n adj channel) with transistor\$1	USPAT	OR	OFF	2005/06/24 08:43
S45	0	cmos with imag\$4 and (transistor\$1) near2 (all)	USPAT	OR	OFF	2005/06/24 06:11
S46	1	cmos with imag\$4 and (transistor\$1) near2 (exclusively)	USPAT	OR	OFF	2005/06/24 06:12
S47	122530	"250"/\$.ccls. 257/4.ccls. "348"/\$.ccls.	USPAT	OR	OFF	2005/06/24 06:12
S48	59	S47 and only.ti.	USPAT	OR	OFF	2005/06/24 06:15
S49	180	S47 and (transistor\$1).ti.	USPAT	OR	OFF	2005/06/24 06:15
S50	178	S49 not S48	USPAT	OR	OFF	2005/06/24 06:26
S51	0	(imag\$4 with chip\$1 with transistor\$1).ti.	USPAT	OR	OFF	2005/06/24 06:38
S52	65	(imag\$4 with transistor\$1).ti.	USPAT	OR	OFF	2005/06/24 06:26
S53	33	S52 not S49	USPAT	OR	OFF	2005/06/24 06:26
S54	41	imag\$4 near2 chip\$1 with transistor\$1	USPAT	OR	OFF	2005/06/24 06:44
S55	5	imag\$4 near2 chip\$1 with (nmos or n adj mos or n adj channel)	USPAT	OR	OFF	2005/06/24 06:48
S56	17	imag\$4 near2 chip\$1 with transistor\$1	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/24 06:44
S57	5	imag\$4 near2 chip\$1 with (nmos or n adj mos or n adj channel)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/24 06:47
S58	10	imag\$4 near2 chip\$1 and (nmos or n adj mos or n adj channel) near3 only	USPAT	OR	OFF	2005/06/24 06:49
S59	461	imag\$4 and (nmos or n adj mos or n adj channel) near3 only	USPAT	OR	OFF	2005/06/24 08:05
S60	461	S59 not S54	USPAT	OR	OFF	2005/06/24 06:49
S61	451	S59 not S58	USPAT	OR	OFF	2005/06/24 06:49
S62	14	imag\$4 and (nmos or n adj mos or n adj channel) near3 only	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/24 07:55
S63	24865	matsushita adj electric	USPAT	OR	OFF	2005/06/24 07:56
S64	6658	S63 and imag\$4	USPAT	OR	OFF	2005/06/24 07:56
S65	142	S64 and (nmos or n adj mos or n adj channel)	USPAT	OR	OFF	2005/06/24 07:56
S66	257	imag\$4 and (pmos or p adj mos or p adj channel) near3 only	USPAT	OR	OFF	2005/06/24 08:10
S67	132	S66 not S59	USPAT	OR	OFF	2005/06/24 08:05
S68	3	imag\$4 and (pmos or p adj mos or p adj channel) near3 only	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/24 08:10

S69	611	imag\$4 with (nmos or n adj mos or n adj channel)	USPAT	OR	OFF	2005/06/24 08:44
S70	294	imag\$4 with (nmos or n adj mos or n adj channel) with transistor\$1	USPAT	OR	OFF	2005/06/24 08:44
S71	317	S69 not S70	USPAT	OR	OFF	2005/06/24 08:52
S72	1	("5345266").PN.	USPAT	OR	OFF	2005/06/24 09:17
S73	466	only adj mos adj transistor\$1	US-PGPUB; USPAT	OR	OFF	2005/06/24 09:17
S74	523	only adj nmos adj transistor\$1	US-PGPUB; USPAT	OR	OFF	2005/06/24 09:17
S75	13	only adj nmos adj transistor\$1 same chip\$1	US-PGPUB; USPAT	OR	OFF	2005/06/24 09:18
S76	281	only adj nmos adj transistor\$1 and chip\$1	US-PGPUB; USPAT	OR	OFF	2005/06/24 09:18
S77	268	S76 not S75	US-PGPUB; USPAT	OR	OFF	2005/06/24 09:18
S78	1	("6188594").PN.	USPAT	OR	OFF	2005/06/24 09:58
S79	84	("4155006" "4233632" "4266237" "4267469" "4310477" "4366455" "4392158" "4407010" "4467400" "4479088" "4486705" "4575638" "4651016" "4656503" "4665422" "4677490" "4680476" "4690606" "4876534" "4975932").PN. OR ("5345266").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/24 13:47
S80	1135	(257/686).CCLS.	USPAT	OR	OFF	2005/06/27 06:33
S81	9	S80 and through adj electrode\$1	USPAT	OR	OFF	2005/06/27 06:33
S82	375	(348/222.1).CCLS.	USPAT	OR	OFF	2005/06/27 06:59